

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

TFW / AF

Applicant: Mark E. Schuelein

Title: SEWAVE-LESS EDGE-TRIGGERED FLIP-FLOP

Docket No.: 1000-0011  
Filed: July 10, 2003  
Examiner: Minh Nguyen

Serial No.: 10/616,810  
Due Date: June 16, 2005  
Group Art Unit: 2816

MS AF

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard and this transmittal document.
- ☒ An Amendment and Response Under 37 CFR § 1.116 (7 Pages)

**Customer Number: 45643**

By

John C. Scott  
Reg. No. 38,613

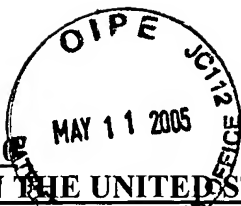
CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 9 day of May, 2005.

MEREDITH MESCHER  
Name

Meredith Mescher  
Signature

(GENERAL)

S/N 10/616,810



PATENT

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Title: SLAVE-LESS EDGE-TRIGGERED FLIP-FLOP

Assignee: Intel Corporation

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AMENDMENT AND RESPONSE UNDER 37 CFR § 1.116

Mail Stop AF  
Commissioner for Patents  
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This responds to the Office Action mailed on March 16, 2005. Please amend the above-identified patent application as follows.